Electronic Patent Application Fee Transmittal								
Application Number:	10595157							
Filing Date:	09-Mar-2006							
Title of Invention:	Method of Manufacturing Multi-Layer Circuit Board							
First Named Inventor/Applicant Name:	Toshiaki Takenaka							
Filer:	Andrew Lawson Dunlap/Lynn Donnelly							
Attorney Docket Number:	2006-0223A							
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Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Basic Filing:								
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Petition:								
Patent-Appeals-and-Interference:								
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Extension-of-Time:								

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Submission- Information Disclosure Stmt	1806	1	180	180
	Tot	180		